

Schedule for a Working SuperModule Electronics System

The University of Chicago

Large Area Picosecond Photodetector Collaboration Meeting
December 9, 2011
Argonne National Laboratory

PSEC DAQ Hardware Schedule

1. Analog Card:

1. PCB Manufacturing – now;
2. Start Assembly and Testing – Dec. 15, 2011;

2. Digital Card:

1. Schematic Design – Dec. 15, 2011;
2. Basic/Workable Firmware for A/C and D/C – Jan. 15, 2012;
3. Layout/Routing – Jan. 20, 2012;
4. PCB Manufacturing / Assembly (1-3 pc.) – Feb. 28, 2012;
5. Testing A/C + D/C – starting March 1st, 2012.

3. Center Card:

1. Schematic Design - 12/23/2011;
2. Basic/Nonfunctional Firmware – Jan. 20, 2012
3. Layout/Routing – Jan. 31, 2012;
4. PCB Manufacturing / Assembly (1-3pc.) – March 8, 2012;
5. Basic/Functional Firmware – March 31, 2012
6. Testing A/C + D/C + C/C – starting March, 2012